

Neuromorphic Memristors with TiO₂ and a-IGZO Bilayer Structure

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In recent years, ReRAM devices have gained significant attention in neuromorphic applications and hardware-based artificial intelligence [1–2]. Specifically, the resistive memory devices exhibit ultrafast read and write speeds, high retention time [3], low voltage operation and low power consumption, emerging an attractive research target from the perspective of the modern low-cost portable devices [4].

Our proposed device performance and physical properties of the fabricated ReRAM devices were assessed at various annealing temperatures. The analysis from XPS results confirms that the device operation was mostly driven by the density of oxygen vacancies in the TiO₂ and a-IGZO bilayer structure. The optimal density of oxygen vacancies in the a-IGZO causes the drift of O²⁻ ions to and from the TiO₂ layer that induced a significant variation in the resistivity of the device, providing switching behavior.

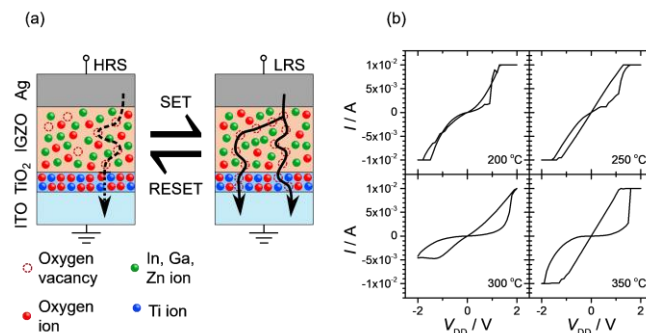


Figure 1(a) Schematic illustration of the mechanism of the conduction in the HRS and LRS (b) schematic drawing of the TiO₂-IGZO ReRAM device with the I-V characteristics.

- [1] P. Yao, H. Wu, B. Gao, J. Tang, Q. Zhang, W. Zhang, J.J. Yang, H. Qian, Fully hardware-implemented memristor convolutional neural network, *Nature* **577** (2020).
- [2] S.P. Adhikari, C. Yang, H. Kim, L.O. Chua, Memristor bridge synapse-based neural network and its learning, *IEEE Trans. Neural Netw. Learn. Syst.* **23** (2012).
- [3] S. Siegel, C. Baeumer, A. Gutsche, M. von Witzleben, R. Waser, S. Menzel, R. Dittmann, Trade-Off Between Data Retention and Switching Speed in Resistive Switching ReRAM Devices, *Adv. Electron. Mater.* **7** (2021).
- [4] N.K. Upadhyay, W. Sun, P. Lin, S. Joshi, R. Midya, X. Zhang, Z. Wang, H. Jiang, J.H. Yoon, M. Rao, M. Chi, Q. Xia, J.J. Yang, A memristor with low switching current and voltage for 1S1R integration and array operation, *Adv. Electron. Mater.* **6** (5) (2020).

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