

Electronic Materials and Photonics

Room 207 A W - Session EM1+CPS+MS+PS+SM+TF-TuA

Advances in Materials and Processes for Devices and Interconnects (FEOL and BEOL)

Moderators: Moon Kim, University of Texas at Dallas, Philip Lee, University of Kentucky

2:15pm EM1+CPS+MS+PS+SM+TF-TuA-1 Carborane-Based Blocking Layers and Plasma Removal Strategies for Area Selective Deposition in Semiconductor Patterning, Raja Sekhar Bale, University of Missouri-Kansas City; *Rupak Thapa*, University of Missouri-Kansas City; *Vamseedhara Vemuri*, Nicholas Strandwitz, Lehigh University; *Anthony Caruso*, *Michelle Paquette*, University of Missouri-Kansas City

Advancing semiconductor scaling to the 2 nm and angstrom-level nodes requires patterning methods that move beyond the resolution, alignment, and cost limits of conventional top-down approaches. Area-selective deposition (ASD) provides a complementary bottom-up strategy, enabling growth only where desired to reduce process steps, minimize defects, and/or improve integration in back-end-of-line (BEOL) fabrication. This work investigates the use of carborane self-assembled monolayers (SAMs) as blocking layers for ASD. Carborane SAMs are thermally stable, mechanically robust, and chemically tunable, making them strong candidates for selective surface modification in semiconductor patterning. Their role is to define surfaces where deposition should be inhibited, while remaining compatible with BEOL conditions. To explore their processing compatibility, the blocking ability of carborane SAMs toward ALD dielectric oxides was studied alongside plasma-based removal. Using CF_4/O_2 and $\text{C}_4\text{F}_8/\text{O}_2/\text{Ar}$ chemistries, we carried out blanket plasma etching of carborane SAMs. This presentation will highlight results on carborane SAM formation, blocking behavior, and plasma response, demonstrating their potential as scalable materials for atomic-scale patterning in next-generation semiconductor manufacturing and their compatibility with advanced BEOL integration.

2:30pm EM1+CPS+MS+PS+SM+TF-TuA-2 Ferroelectricity in Atomic Layer Deposited Wurtzite Zinc Magnesium Oxide $\text{Zn}_{1-x}\text{Mg}_x\text{O}$, Benjamin Aronson, University of Virginia; *Kyle Kelley*, Oak Ridge National Laboratory; *Ece Gunay*, Carnegie Mellon University; *Ian Mercer*, Penn State University; *Bogdan Dryzhakov*, Oak Ridge National Laboratory; *Susan Trolrier-McKinstry*, *Jon-Paul Maria*, Penn State University; *Elizabeth Dickey*, Carnegie Mellon University; *Jon Ihlefeld*, University of Virginia

Ferroelectric wurtzites have garnered interest in the scientific community since first reported in 2019. $\text{Zn}_{1-x}\text{Mg}_x\text{O}$ has shown promise due its low coercive field (2–3 MV/cm) relative to other wurtzites, integrability on flexible polymer substrates, and complementary metal–oxide–semiconductor (CMOS) and back–end–of–line (BEOL) compatible deposition temperatures as low as room temperature. However, the majority of ferroelectric wurtzite thin films – including $\text{Zn}_{1-x}\text{Mg}_x\text{O}$ – have been fabricated using physical vapor deposition (PVD) techniques, which features largely directional growth. Due to the use of high aspect ratio structures in non-volatile memory devices, the ability to conformally deposit ferroelectric wurtzites will contribute to BEOL integration. Atomic layer deposition (ALD) presents an opportunity to overcome this outstanding challenge due to its sequential, self-limiting growth. In this work, $\text{Zn}_{1-x}\text{Mg}_x\text{O}$ thin films with compositions between $x = 0$ and $x = 0.58$ were grown on platinized silicon substrates using plasma-enhanced atomic layer deposition. Films were characterized using X-ray diffraction (XRD), transmission electron microscopy (TEM), and piezoresponse force microscopy (PFM). All films deposited featured a singular out-of-plane c -axis textured wurtzite structure. The c/a ratio decrease with increasing Mg content indicates the increasing structural distortion. Film structure and structural distortions were further reinforced and visualized via TEM. PFM amplitude and phase hysteresis loops demonstrated polarization reversal in the $x = 0.46$ and $x = 0.58$ films. Ultimately, this finding presents opportunities to further mature the $\text{Zn}_{1-x}\text{Mg}_x\text{O}$ processing space in which ferroelectric switching is possible, as well as explore ALD of other ferroelectric wurtzites.

2:45pm EM1+CPS+MS+PS+SM+TF-TuA-3 Harnessing Nitrogen-Rich Interfaces in AlN Ferroelectrics, Ian Mercer¹, Erdem Ozdemir, Chloe Skidmore, Benjamin Debastiani, Kazuki Okamoto, Penn State University; *Sebastian Calderon*, *Elizabeth Dickey*, Carnegie Mellon University; *Susan Trolrier-McKinstry*, *Jon-Paul Maria*, Penn State University

The importance of interface preparation in the nitride semiconductor and thin film community has long been recognized as critical in controlling nucleation and properties. These AlN ferroelectrics are an enticing pathway toward integrated energy-efficient robust non-volatile memory, displaying CMOS chemical compatibility, large polarizations, and BEOL processing. Although this has not been fully realized in the relatively recent nitride wurtzite ferroelectric community, current convention stems from strictly polar systems like GaN and AlN. However, there is a clear opportunity in engineering electrode interfaces in these systems to aid in film nucleation, reduced leakage, and extended fatigue lifetimes. In this work, we discuss the influence of surface nitriding on a variety of relevant substrates prior to film deposition to enhance film texture and electrical properties. Adding the surface nitriding leads to a discussion on whether nitrogen-rich interfaces can compensate for nitrogen vacancies that migrate to electrode interfaces during cycling. By depositing top and bottom metal nitride electrodes, we investigate the benefits in the electrical properties versus metallic electrodes. Reactive RF magnetron sputtering is employed to co-sputter AlN ferroelectrics. X-ray diffraction (XRD) is used to display c -axis texture, while hysteresis (PE), leakage (PUND), and fatigue measurements are used to characterize the electrical properties. Etching/SEM is also used to display partial switching, exploiting the n -polar fast etch in KOH solutions, which helps visualize the effects of nitrogen-rich interfaces. Furthermore, this study reinforces the functionality of interface engineering in AlN ferroelectrics at both the top and bottom electrode interfaces. The importance of this work is that all films in this class may benefit from nitrogen-rich interfaces.

3:00pm EM1+CPS+MS+PS+SM+TF-TuA-4 Selective Etching of GaN Over AlGaIn and Monitoring via Optical Emission Spectroscopy, Michael Thomas, Patrick Wellenius, Spyridon Pavlidis, North Carolina State University

Achieving etch selectivity between GaN and AlGaIn is critical for the repeatable fabrication of enhancement-mode AlGaIn/GaN High Electron Mobility Transistors (HEMTs). The selectivity can be tuned by varying the O_2 content in a Cl_2 -based etch. In this work, we explore the etch process parameter space that affects selectivity and explore how *in-situ* optical emission spectroscopy (OES) can be used as an indicator of chamber and plasma conditions over time.

Two epitaxial structures on sapphire were used. The first is a thin film of GaN (control). The second is a device-relevant AlGaIn/GaN heterojunction with a GaN cap layer. Following photolithography, samples of each type were etched simultaneously in an Oxford Instruments Plasmapro 100 Cobra inductively coupled plasma (ICP) to eliminate run-to-run variation from the selectivity determination. The total etch time was varied by gas composition to keep the HEMT sample etch depth within the AlGaIn front barrier. Etch step heights were measured via atomic force microscopy (AFM) in an Oxford Instruments Asylum Research MFP-3D Origin AFM. Using an OceanOptics USB4000 Spectrometer, OES signals were collected with 1 s integration every 60 s during chamber cleaning and conditioning, and every 30 s during the final etches for each composition.

During initial experiments, the chamber pressure, ICP power, and table RF power were all kept constant at 15 mTorr, 500 W, and 25 W, respectively. The total gas flow was kept constant at 50 sccm, and Cl_2 was further kept constant at 35 sccm. The remaining 15 sccm were split between O_2 and Ar, with three tests being done at 0/15, 2/13, and 4/11 sccm of O_2/Ar respectively. An initial peak selectivity of 3.45:1 was measured with 2 sccm O_2 . The OES signal confirms O_2 emission brightness changes as expected with flow rate. To further improve the selectivity, we will report on the etch characteristics across a wider parameter space, including varying the Cl_2 content of the plasma, the total gas flow rate, the chamber pressure, ICP power and substrate size. Moreover, we explore how the OES's utility can be leveraged to assess the effectiveness of pre-etch chamber conditioning to improve both selectivity and repeatability. The results of this study are expected to boost the yield and performance of AlGaIn/GaN HEMTs.

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