Basal Plane Dislocation Mitigation via Annealing and Growth Interrupts

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Basal plane dislocations (BPD) in SiC are high-voltage bipolar device killers that source Shockley-type stacking faults in the presence of an electron-hole plasma [1]. Multiple research groups have been successful in mitigating their propagation from the substrate into the epitaxial layer [2-5]. While these are sufficient for typical SiC devices, for high pulsed power current density or high surge current capability applications, the injected carrier concentration is significant enough to expand converted BPDs. Here, we will report results from comparisons of H_2 etching to Ar annealing and the use of H_2 versus Ar during growth interrupts to prevent BPD expansion.

SiC epitaxial layers were grown using a CVD reactor on 4° off-axis substrates toward the [11-20] that are known to have BPDs. A H₂ etch or Ar anneal was performed before the buffer layer (BL) growth while a growth interrupt in H₂ or Ar was conducted prior to the intentionally low doped drift layer. Ultraviolet photoluminescence (UVPL) imaging was used to image the samples before and after UV stressing up to 13 kWcm⁻².

The H₂ etch and H₂ growth interrupt prevented BPDs from expanding under UV stress of 13 kWcm⁻² and it is believed that the H₂ treatment specifically inhibited this expansion. To confirm the role of H₂, we performed a growth using the same conditions as the H₂ etch/interrupt, however, an Ar anneal was used instead of a H₂ etch and the growth interrupt was conducted in an Ar atmosphere instead of H₂. The sample was UV stressed up to 1000 Wcm⁻² and it was found that four BPD expanded from the substrate into the epilayer. For comparison, a sample grown with a double H₂ etch (before the buffer layer growth and drift layer) and a sample grown with a H₂ etch plus H₂ growth interrupt did not produce faulting at the same power density. This indicates that H₂ influences BPD expansion. We will present detailed parametric results of samples grown with various etching/ annealing, growth interrupts, anneal times, buffer layer thickness, gas flow rates and interrupt temperature, both in H₂ and Ar.

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Fig. 1. (a) Growth schedule including a H_2 etch or Ar anneal prior to the buffer layer and a growth interrupt in H_2 or Ar before the drift layer. (b) Cross sectional view of the epitaxial layers grown with a H_2 etch or Ar anneal and interrupt for BPD reduction.



Fig. 2. UVPL images of epitaxial sample with Ar anneal before BL and growth interrupt in Ar before drift layer (a) before stressing (no BPD in epilayer) and (b) after UV stressing at 1 kWcm⁻², showing a BPD that expanded from the substrate.