

Figure 1

Etching of a-Si at Different Positive Sample Voltages vs Time

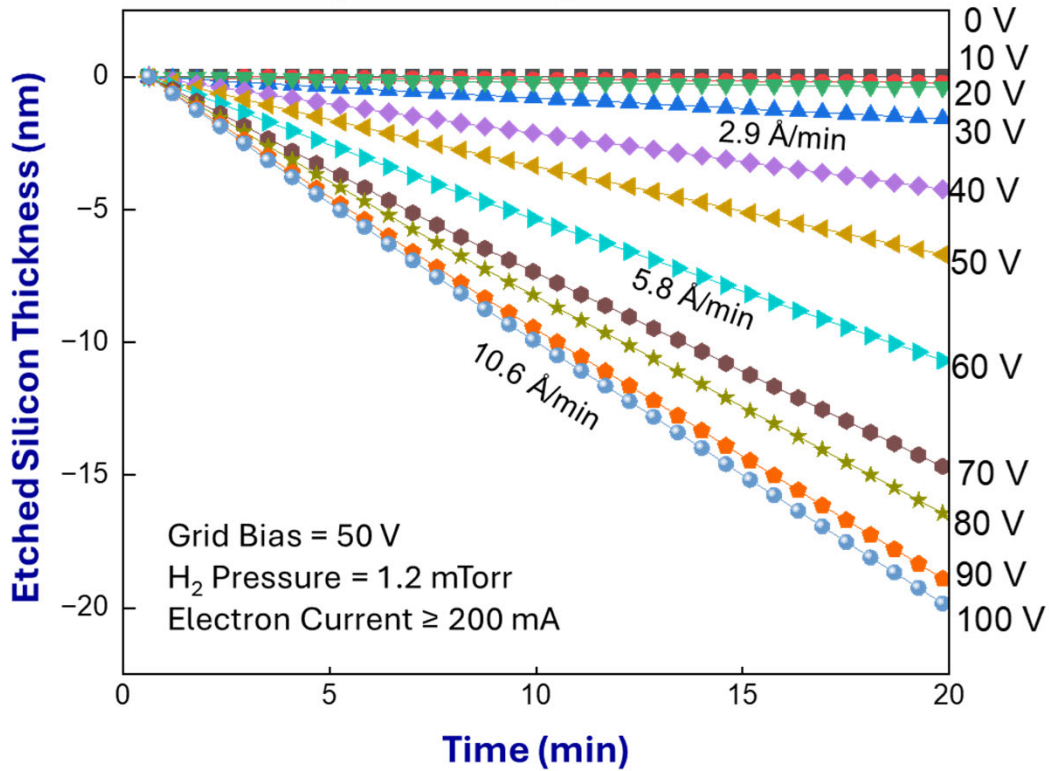


Figure 2

Schematic for Electron-Enhanced Si Etching with H_2 Background Gas

