Supplemental Data

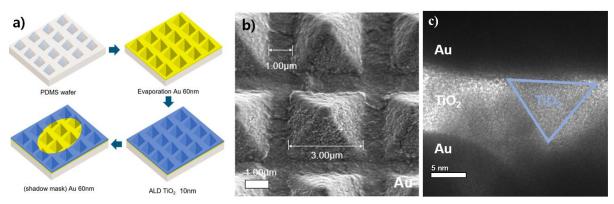


Fig 1. a) Schematic diagram of fabrication process of RRAM device using pyramid<u>-structured</u> PDMS. b) A bird eye view SEM image of the surface of the completed device. c) A cross<u>-</u>sectional HRTEM image of the Au/TiO₂/Au structure.

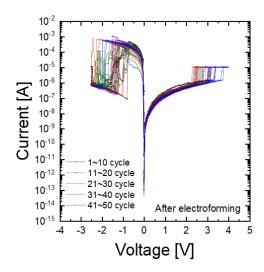


Fig 2. Stable RS behavior after electroforming until 50 switching cycles.