

Monday Morning, July 22, 2019

Plenary Session

Room Grand Ballroom A-G - Session PS1-MoM

ALD Plenary Session

Moderators: Sumit Agarwal, Colorado School of Mines, Dennis Hausmann, Lam Research

8:45am **PS1-MoM-2 Atomic Scale Processing: From Understanding to Innovation**, *Erwin Kessels*, Eindhoven University of Technology, Netherlands **INVITED**

Atomic scale processing is the collective term for processing methods that are currently more and more being explored – and industrially employed – to prepare thin films and nanostructures in a highly controllable way with atomic scale precision. Atomic layer deposition (ALD) and atomic layer etching (ALE) are two prominent examples but also state-of-the-art nanopatterning and area-selective deposition (ASD) approaches can in many cases be regarded as examples of atomic scale processing. In this presentation, I will address several recent and emerging trends in atomic scale processing and discuss how understanding of the underlying mechanisms has contributed to important innovations in the field, either in research or in industry. Application areas to be addressed are especially nanoelectronics and photovoltaics which I will discuss from my own experience and perspective. Topics to be discussed are: (i) the growing importance of plasma ALD, aspects such as conformality and the role (and control) of ions; (ii) the necessity of in situ metrology methods (e.g. in situ spectroscopic ellipsometry) for process monitoring and advanced surface spectroscopy for obtaining more quantitative insight into fundamental parameters; (iii) the use of ALD in nanopatterning and the interest in area-selective ALD including those combinations with intermediate etch steps; and (iv) the need for isotropic and anisotropic ALE, either thermal or plasma-based.

9:30am **PS1-MoM-5 Plenary-ALD Elam**, *Jeffrey W. Elam*, Argonne National Laboratory **INVITED**

Plenary Session

Room Grand Ballroom A-G - Session PS2-MoM

ALE Plenary Session

Moderators: Craig Huffman, Micron, Gottlieb S. Oehrlein, University of Maryland, College Park

11:00am **PS2-MoM-11 Mapping the Future Evolution of Atomic Scale Processing to enable the World of Artificial Intelligence**, *Eric A. Joseph*, IBM T.J. Watson Research Center **INVITED**

Advances in the semiconductor industry, historically based on Moore's Law and Dennard scaling, have become progressively challenging as device technology moves beyond the 7nm node. The ever-continuing trend to shrink device size, coupled with the introduction of novel materials, multi-component materials and/or nanoscale materials has driven the need for the ultimate solution: atomic scale precision. To meet this demand, considerable work has been underway to incorporate advances in atomic layer etching (ALE), atomic layer deposition (ALD), and area selective techniques to meet process requirements. However, as future technology undergoes a paradigm shift away from Moore's Law towards accelerator technologies for AI applications, the types of process driven challenges will also change. This transition will require a revised focus on process capability, expanding beyond traditional process enhancements, to minimizing process induced device performance degradation. Examples of this paradigm shift will be discussed in detail and a vision for the future challenges of atomic scale processes will be reviewed.

Author Index

Bold page numbers indicate presenter

— E —

Elam, J: PS1-MoM-5, **1**

— J —

Joseph, E: PS2-MoM-11, **1**

— K —

Kessels, E: PS1-MoM-2, **1**